

## Description

These power modules are a series of high performance, 8-A rated, Integrated Switching Regulators (ISRs), housed in a low cost 14-Pin SIP (Single In-line Package). Operating from either a 3.3V or 5V standard power bus, the PT6520 series produces a high-output, low-voltage power source for the industry's latest high-speed, microprocessors, ASICs, & DSPs. This allows for the easy integration of these new low-voltage ICs into existing 3.3V or 5V systems without re-designing the power supply.

The PT6520 series features an output On/Off standby pin and output short-circuit protection.

## Features

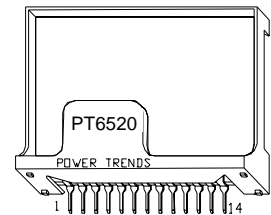
- 8A Rated Output Current
- Single Device: 3.3V/5V Input
- High Efficiency (92% for PT6521)
- Small Footprint (0.75 in<sup>2</sup>, Suffix 'P')
- Output On/Off Standby Control
- Output Short-Circuit Protection
- Adjustable Output Voltage
- Soft Startup
- 16-pin Mount Option (Suffixes L, M, Q, & F)

## Ordering Information

PT6521□	= 3.3 Volts
† PT6522□	= 1.5 Volts
† PT6523□	= 2.5 Volts
† PT6525□	= 2.1 Volts
† PT6526□	= 1.8 Volts
† PT6527□	= 1.2 Volts

† 3.3V Input Bus Capable

Note:  
Back surface of  
product is electrically  
conductive



## PT Series Suffix (PT1234x)

Case/Pin Configuration	Order Suffix	Package Code *
Vertical	<b>P</b>	(EED)
Horizontal	<b>D</b>	(EEA)
SMD	<b>E</b>	(EEC)
Horizontal, 2-Pin Tab	<b>M</b>	(EEM)
SMD, 2-Pin Tab	<b>L</b>	(EEL)
Horizontal, 2-Pin Ext Tab	<b>Q</b>	(EEQ)
SMD, 2-Pin Ext Tab	<b>F</b>	(EEF)
Vertical, Side Tab	<b>R</b>	(EEE)
Horizontal, Side Tab	<b>G</b>	(EEG)
SMD, Side Tab	<b>B</b>	(EEK)

\* Previously known as package styles 400/410.

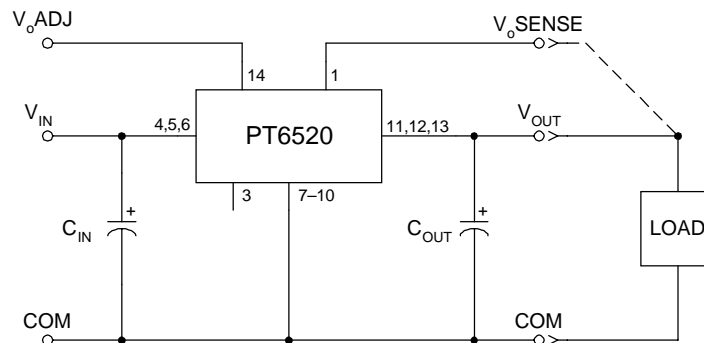
(Reference the applicable package code drawing for the dimensions and PC board layout)

## Pin-Out Information

Pin	Function
1	Remote Sense
2	Do Not Connect
3	STBY*
4	V <sub>in</sub>
5	V <sub>in</sub>
6	V <sub>in</sub>
7	GND
8	GND
9	GND
10	GND
11	V <sub>out</sub>
12	V <sub>out</sub>
13	V <sub>out</sub>
14	V <sub>out</sub> Adjust

\* For further information, see application notes.

## Standard Application



**Specifications** (Unless otherwise stated,  $T_a = 25^\circ\text{C}$ ,  $V_{in} = 5\text{V}$ ,  $C_{in} = 330\mu\text{F}$ ,  $C_{out} = 330\mu\text{F}$ , and  $I_o = I_{o\text{max}}$ )

Characteristic	Symbol	Conditions	PT6520 SERIES			Units
			Min	Typ	Max	
Output Current	$I_o$	Over $V_{in}$ range	0.1 <sup>(1)</sup>	—	8.0	A
Input Voltage Range	$V_{in}$	Over $I_o$ Range $V_o = 3.3\text{V}$ $V_o \leq 2.5\text{V}$	4.5 3.1	—	5.5 5.5	VDC
Set Point Voltage Tolerance	$V_o \text{ tol}$		—	$\pm 1$	$\pm 1.5$	% $V_o$
Temperature Variation	$\text{Reg}_{\text{temp}}$	$-40^\circ \leq T_a \leq +85^\circ\text{C}$ , $I_o = I_{o\text{min}}$	—	$\pm 0.5$	—	% $V_o$
Line Regulation	$\text{Reg}_{\text{line}}$	Over $V_{in}$ range	—	$\pm 5$	$\pm 10$	mV
Load Regulation	$\text{Reg}_{\text{load}}$	Over $I_o$ range	—	$\pm 5$	$\pm 10$	mV
Total Output Voltage Variation	$\Delta V_{o\text{tot}}$	Includes set-point, line, load, $-40^\circ \leq T_a \leq +85^\circ\text{C}$	—	$\pm 2$	$\pm 3$	% $V_o$
Efficiency	$\eta$	$I_o = 3.0\text{A}$	$V_o = 3.3\text{V}$	—	92	%
			$V_o = 2.5\text{V}$	—	88	
			$V_o = 2.1\text{V}$	—	85	
			$V_o = 1.8\text{V}$	—	82	
			$V_o = 1.5\text{V}$	—	80	
			$V_o = 1.2\text{V}$	—	75	
		$I_o = 8.0\text{A}$	$V_o = 3.3\text{V}$	—	89	%
			$V_o = 2.5\text{V}$	—	85	
			$V_o = 2.1\text{V}$	—	82	
			$V_o = 1.8\text{V}$	—	78	
			$V_o = 1.5\text{V}$	—	75	
			$V_o = 1.2\text{V}$	—	71	
$V_o$ Ripple (pk-pk)	$V_r$	20MHz bandwidth	—	35	—	mV <sub>pp</sub>
Transient Response	$t_{tr}$	1A/ $\mu\text{s}$ load step, 50% to 100% $I_{o\text{max}}$	—	50	—	$\mu\text{s}$
	$\Delta V_{tr}$	$V_o$ over/undershoot	—	$\pm 70$	—	mV
Short Circuit Threshold	$I_{sc}$ threshold		—	12	22.5	A
Switching Frequency	$f_s$	Over $V_{in}$ and $I_o$ range	300	350	400	kHz
Remote On/Off (Pin 1)		Referenced to $-V_{in}$ (pin 7)				
Input High Voltage	$V_{IH}$		—	—	Open <sup>(2)</sup>	V
Input Low Voltage	$V_{IL}$		-0.1	—	+0.4	
Input Low Current	$I_{IL}$		—	-0.5	—	mA
Standby Input Current	$I_{in \text{ standby}}$	pins 3 & 7 connected	—	15	25	mA
External Output Capacitance	$C_{out}$	See application schematic	330	—	5,000	$\mu\text{F}$
External Input Capacitance	$C_{in}$	See application schematic	330	—	—	$\mu\text{F}$
Operating Temperature Range	$T_a$	Over $V_{in}$ range	-40	—	+85 <sup>(3)</sup>	$^\circ\text{C}$
Storage Temperature	$T_s$	—	-40	—	+125	$^\circ\text{C}$
Reliability	MTBF	Per Bellcore TR-332 50% stress, $T_a = 40^\circ\text{C}$ , ground benign	6.3	—	—	$10^6$ Hrs
Mechanical Shock	—	Per Mil-Std-883D, method 2002.3, 1ms, half-sine, mounted to a fixture	—	500	—	G's
Mechanical Vibration	—	Suffixes P, D, & E	—	7.5	—	G's
Mil-Std-883D, 20-2000Hz	—	Suffixes L & M	—	TBD <sup>(4)</sup>	—	
Weight	—	Suffixes P, D, & E	—	12.5	—	grams
		Suffixes R, G & B	—	16.5	—	
		Suffixes L & M	—	15.25	—	
		Suffixes Q & F	—	22	—	
Flammability	—	Materials meet UL 94V-0				

**Notes:** (1) The ISR will operate at no load with reduced specifications.

(2) The STBY\* control (pin 3) has an internal pull-up and if it is left open circuit the module will operate when input power is applied. The open-circuit voltage is typically 12.6V, and maybe as high as 15V. Refer to the application notes for other interface considerations.

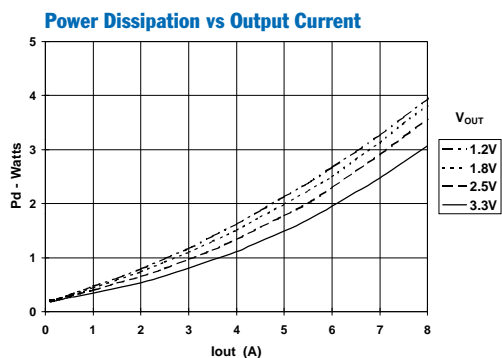
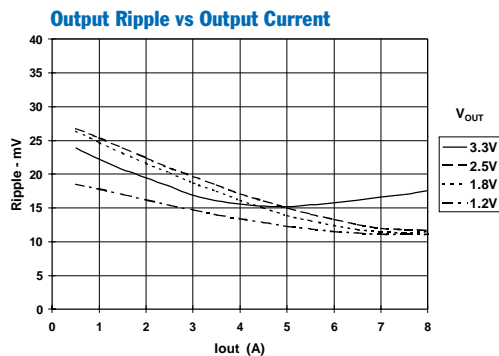
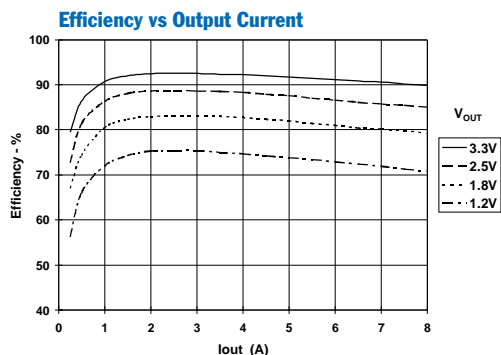
(3) See Safe Operating Area curves or contact the factory for the appropriate derating.

(4) The tab pins on the 16-pin mount package types (suffixes L &amp; M) must be soldered. For more information see the applicable package outline drawing.

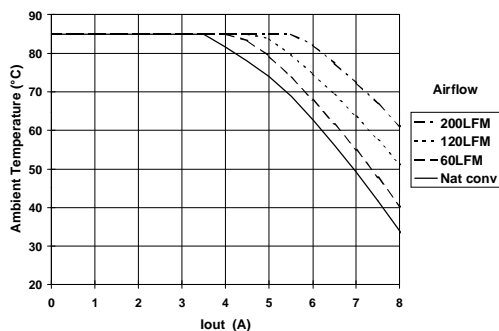
**Input/Output Capacitors:** The PT6520 series requires a 330 $\mu\text{F}$  capacitor at both the input and output for proper operation in all applications. In addition, the input capacitance ( $C_{in}$ ) must be rated for a minimum of 1.2Arms ripple current rating. For transient or dynamic load applications, additional output capacitance ( $C_{out}$ ) may be necessary. The maximum allowable output capacitance is 5,000 $\mu\text{F}$ . For more information consult the related application note on capacitor recommendations.

8-A 5-V/3.3-V Input Adjustable ISR  
with Short-Circuit protection

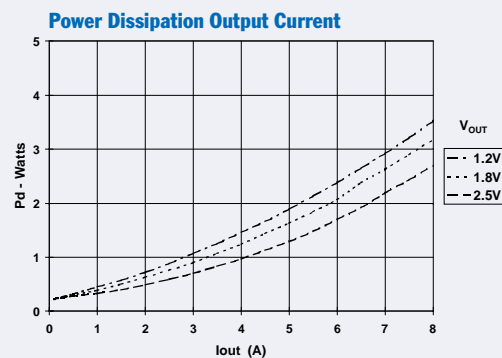
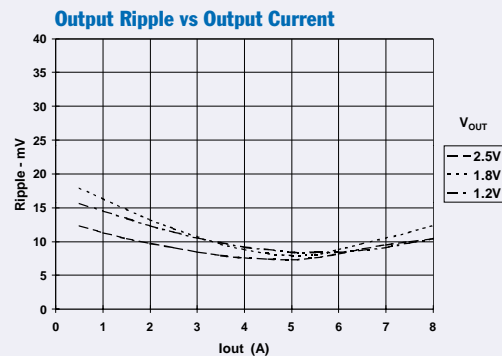
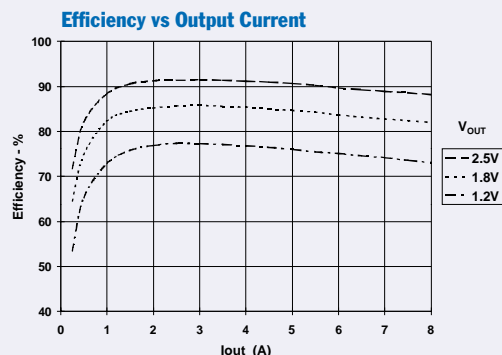
Characteristic Data;  $V_{in} = 5.0V$  (See Note A)



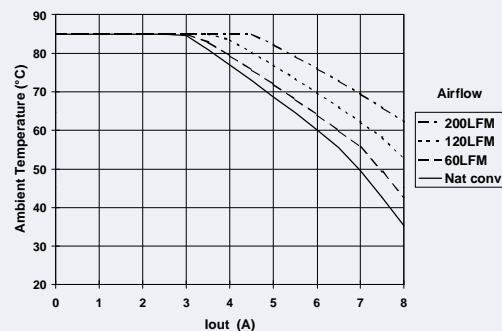
Safe Operating Area;  $V_{in} = 5V$  (See Note B)



Characteristic Data;  $V_{in} = 3.3V$  (See Note A)



Safe Operating Area;  $3.3V$  (See Note B)



**Note A:** Characteristic data has been developed from actual products tested at 25°C. This data is considered typical data for the Converter.

**Note B:** SOA curves represent the conditions at which internal components are at or below the manufacturer's maximum operating temperatures

### Using the Standby Function of the PT6520 Series of Integrated Switching Regulators

The PT6520 series of power modules are high efficiency regulators that operate off either a 3.3V or 5V input bus voltage. These regulators incorporate a *Standby* function, which may be used in applications that require power-up/shutdown sequencing, and wherever there is a requirement for the output status of the module to be controlled by external circuitry.

The standby function is provided by the *STBY*\* control, pin 3. If pin 3 is left open-circuit<sup>1</sup> the regulator operates normally, and provides a regulated output when a valid supply voltage is applied to *V<sub>in</sub>* (pins 4–6) with respect to GND (pins 7–10). If a low voltage<sup>2</sup> is then applied to pin 3 the regulator output will be disabled and the input current drawn by the ISR will be reduced to about 15mA<sup>3</sup>. The standby control may also be used to hold-off the regulator output during the period that input power is applied.

Pin 3 is ideally controlled with an open-collector (or open-drain) discrete transistor (See Figure 1). The open-circuit voltage is typically 12.6V. Table 1 gives the circuit parameters for this control input.

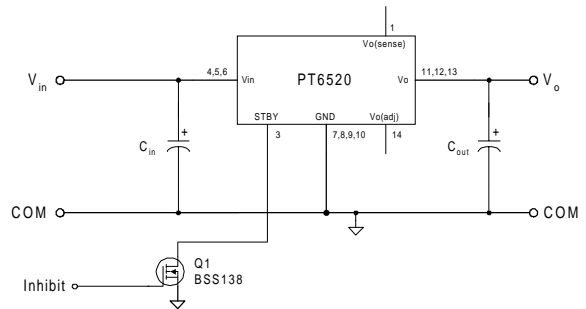
**Table 1 Standby Control Requirements** <sup>(2, 3)</sup>

Parameter	Min	Typ	Max
Input Low ( <i>V<sub>IL</sub></i> )	–0.1V		+0.4V
<i>I<sub>stby</sub></i> (pin 3 =ground)		–0.5mA	
<i>V<sub>stby</sub></i> (open circuit)		12.6V	15V

#### Notes:

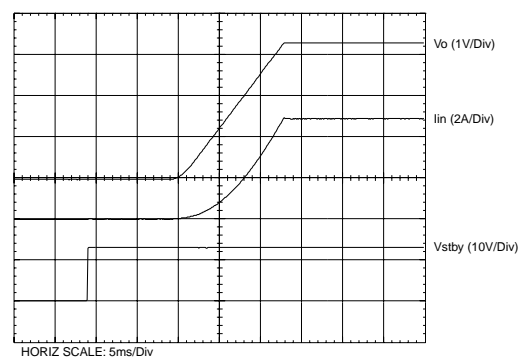
- 1 The standby control input requires no external pull-up resistor. The open-circuit voltage of the *STBY*\* pin is typically 12.6V.
- 2 The standby control input is Not compatible with TTL or other devices that incorporate a totem-pole output drive. Use only a true open-collector device, preferably a discrete bipolar transistor (or MOSFET). To ensure the regulator output is disabled, the control pin must be pulled to less than 0.4Vdc with a low-level 0.5mA sink to ground.
- 3 When the regulator output is disabled the current drawn from the input source is typically reduced to 15mA.

**Figure 1**



**Turn-On Time:** In the circuit of Figure 1, turning *Q<sub>1</sub>* on applies a low voltage to the *STBY* control (pin 3) and disables the regulator output. Correspondingly, turning *Q<sub>1</sub>* off removes the low-voltage signal and enables the output. Once enabled, the output will typically experience a 10–15ms delay followed by a predictable ramp-up of voltage. The regulator should provide a fully regulated output voltage within 40ms. The waveform of Figure 2 shows the output voltage and input current waveforms of a PT6521 (3.3V) following the turn-off of *Q<sub>1</sub>*. The turn off of *Q<sub>1</sub>* corresponds to the rise in *V<sub>stby</sub>*. The waveforms were measured with a 5Vdc input voltage, and 4.5A resistive load.

**Figure 2**



## Adjusting the Output Voltage of the PT6520 Series of Integrated Switching Regulators

The output voltage of the PT6520 series of integrated switching regulators (ISRs) may be adjusted higher or lower than the factory trimmed pre-set voltage with the addition of a single external resistor.<sup>1</sup> Table 1 gives the allowable adjustment range for each model in the series as  $V_a$  (min) and  $V_a$  (max).

**Adjust Up:** An increase in the output voltage is obtained by adding a resistor  $R_2$ , between pin 14 ( $V_o$  adjust) and pins 7-10 (GND).

**Adjust Down:** Add a resistor ( $R_1$ ), between pin 14 ( $V_o$  adjust) and pin 1 ( $V_o$ (sense))<sup>3</sup>.

Refer to Figure 1 and Table 2 for both the placement and value of the required resistor, either ( $R_1$ ) or  $R_2$  as appropriate.

The values of ( $R_1$ ) [adjust down], and  $R_2$  [adjust up], can also be calculated using the following formulas.

$$(R_1) = \frac{R_o (V_a - V_r)}{V_o - V_a} - R_s \quad \text{k}\Omega$$

$$R_2 = \frac{R_o \cdot V_r}{V_a - V_o} - R_s \quad \text{k}\Omega$$

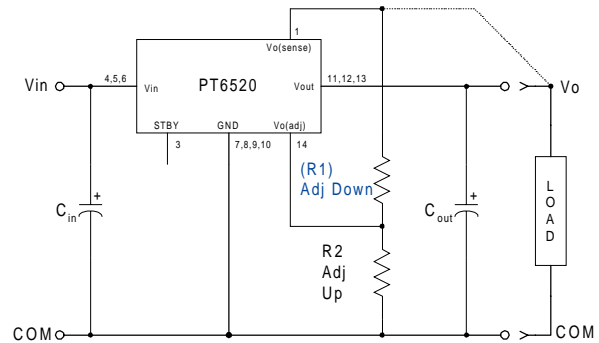
Where:  $V_o$  = Original output voltage  
 $V_a$  = Adjusted output voltage  
 $V_r$  = The reference voltage (Table 1)  
 $R_o$  = The multiplier resistance (Table 1)  
 $R_s$  = The internal series resistance (Table 1)

**Table 1**

### ADJUSTMENT AND FORMULA PARAMETERS

Series Pt #	PT6527	PT6522	PT6526	PT6525	PT6523	PT6521
$V_o$ (nom)	1.2	1.5	1.8	2.1	2.5	3.3
$V_a$ (min)	1.09	1.47	1.75	1.95	2.25	2.75
$V_a$ (max)	1.52	1.73	2.05	2.45	2.85	3.75
$V_r$ (V)	0.8	1.27	1.27	1.27	1.27	1.27
$R_o$ (k $\Omega$ )	10.0	10.2	10.0	10.0	10.0	10.0
$R_s$ (k $\Omega$ )	24.9	49.9	49.9	33.2	33.2	24.9

**Figure 1**



### Notes:

1. Use only a single 1% resistor in either the ( $R_1$ ) or  $R_2$  location. Place the resistor as close to the ISR as possible.
2. Never connect capacitors from  $V_o$  adjust to either GND,  $V_{out}$ , or the Remote Sense pin. Capacitance added to the  $V_o$  adjust pin will affect the stability of the ISR.
3. If the Remote Sense feature is not being used, the resistor ( $R_1$ ) may be connected between pin 14 ( $V_o$  adjust) and pins 11–13 ( $V_{out}$ ).
4. Adjusting the output voltage of the PT6523 (2.5V model) higher than the factory pre-trimmed output voltage may increase the minimum input voltage specified for the part. This model must comply with the following requirements.

### PT6523:

$V_{in(min)} = (V_a + 0.5)V$  or 3.1V, whichever is greater.

## PT6520 Series

Table 2

## PT6520 ADJUSTMENT RESISTOR VALUES

Series Pt #	PT6527	PT6522	PT6526	PT6525	PT6523	Series Pt #	PT6521
<b>V<sub>o</sub> (nom)</b>	1.2V	1.5V	1.8V	2.1V	2.5V	<b>V<sub>o</sub> (nom)</b>	3.3V
<b>V<sub>a</sub> (req'd)</b>						<b>V<sub>a</sub> (req'd)</b>	
1.1	(5.1)kΩ					2.75	(2.0)kΩ
1.15	(45.1)kΩ					2.8	(5.7)kΩ
1.2						2.85	(10.2)kΩ
1.25	135.0kΩ					2.9	(15.9)kΩ
1.3	55.1kΩ					2.95	(23.1)kΩ
1.35	28.4kΩ					3.0	(32.8)kΩ
1.4	15.1kΩ					3.05	(46.3)kΩ
1.45	7.1kΩ					3.1	(66.6)kΩ
1.47	4.7kΩ	(18.1)kΩ				3.15	(100.0)kΩ
1.5	1.8kΩ					3.2	(168.0)kΩ
1.55		209.0kΩ				3.25	(371.0)kΩ
1.6		79.6kΩ				3.3	
1.65		36.5kΩ				3.35	229.0kΩ
1.7		14.9kΩ				3.4	102.0kΩ
1.75			(46.1)kΩ			3.45	59.8kΩ
1.8						3.5	38.6kΩ
1.85			204.0kΩ			3.55	25.9kΩ
1.9			77.1kΩ			3.6	17.4kΩ
1.95			34.8kΩ	(12.1)kΩ		3.65	11.4kΩ
2.0			13.6kΩ	(39.8)kΩ		3.7	6.9kΩ
2.05				(123.0)kΩ		3.75	3.3kΩ
2.1							
2.15				221.kΩ			
2.2				93.8kΩ			
2.25				51.5kΩ	(6.0)kΩ		
2.3				30.3kΩ	(18.3)kΩ		
2.35				17.6kΩ	(38.8)kΩ		
2.4				9.1kΩ	(79.8)kΩ		
2.45				3.1kΩ	(203.0)kΩ		
2.5							
2.55					221.0kΩ		
2.6					93.8kΩ		
2.65				(See Note 4)	51.5kΩ		
2.7					30.3kΩ		
2.75					17.6kΩ		
2.8					9.1kΩ		
2.85					3.1kΩ		

R1 = (Blue) R2 = Black

## Capacitor Recommendations for the PT6520 Series of Integrated Switching Regulators

### Input Capacitors:

The recommended input capacitance is determined by 1.0 ampere minimum ripple current rating and 330 $\mu$ F minimum capacitance (300 $\mu$ F for Oscon® or low ESR tantalum). Ripple current and <100m $\Omega$  equivalent series resistance (ESR) values are the major considerations, along with temperature, when designing with different types of capacitors. Tantalum capacitors have a recommended minimum voltage rating of 2 $\times$  the maximum DC voltage + AC ripple. This is necessary to insure reliability for input voltage bus applications.

### Output Capacitors:

The ESR of the required capacitor ( $C_{out}$ ) must not be greater than 150m $\Omega$ . Electrolytic capacitors have poor ripple performance at frequencies greater than 400kHz but excellent low frequency transient response. Above the ripple frequency, ceramic capacitors are necessary to improve the transient response and reduce any high frequency noise components apparent during higher current excursions. Preferred low ESR type capacitor part numbers are identified in Table 1.

### Tantalum Capacitors

Tantalum type capacitors may be used for the output but only the AVX TPS series, Sprague 593D/594/595 series or Kemet T495/T510 series. These capacitors are recommended over many other tantalum types due to their higher rated surge, power dissipation, and ripple current capability. As a caution the TAJ series by AVX is not recommended. This series has considerably higher ESR, reduced power dissipation, and lower ripple current capability. The TAJ series is less reliable than the AVX TPS series when determining power dissipation capability. Tantalum or Oscon® types are recommended for applications where ambient temperatures fall below 0°C.

### Capacitor Table

Table 1 identifies the characteristics of capacitors from a number of vendors with acceptable ESR and ripple current (rms) ratings. The number of capacitors required at both the input and output buses is identified for each capacitor type.

*This is not an extensive capacitor list. Capacitors from other vendors are available with comparable specifications. Those listed are for guidance. The RMS ripple current rating and ESR (Equivalent Series Resistance at 100kHz) are critical parameters necessary to insure both optimum regulator performance and long capacitor life.*

**Table 1: Input/Output Capacitors**

Capacitor Vendor/ Component Series	Capacitor Characteristics					Quantity		Vendor Number
	Working Voltage	Value( $\mu$ F)	(ESR) Equivalent Series Resistance	85°C Maximum Ripple Current(Irms)	Physical Size(mm)	Input Bus	Output Bus	
Panasonic FC	25V	560 $\mu$ F	0.0065 $\Omega$	1205mA	12.5x15	1	1	EEUFC1E561S
	35V	390 $\mu$ F	0.065 $\Omega$	1205mA	12.5x15	2	1	EEUFC1V391S
	35V	330 $\mu$ F	0.117 $\Omega$	555mA	8x11.5	N/R	1	EEUFC1C331
United Chemi-Con LXV/FS/LXZ	16V	330 $\mu$ F	0.120 $\Omega$	555mA	8x12	N/R	1	LXZ16VB331M8X12LL
	35V	470 $\mu$ F	0.052 $\Omega$	1220mA	10x20	1	1	LXZ35VB471M10X20LL
	10V	330 $\mu$ F	0.025 $\Omega$	3500mA	10x10.5	1	1	10FS330M
	20V	150 $\mu$ F	0.030+2 $\Omega$	3200mA	10x10.5	2	2	20FS150M
Nichicon PL/ PM	35V	560 $\mu$ F	0.048 $\Omega$	1360mA	16x15	1	1	UPL1V561MHH6
	35V	330 $\mu$ F	0.065+2 $\Omega$	1020mA	12.5x15	1	1	UPL1V331MHH6
	50V	470 $\mu$ F	0.046 $\Omega$	1470mA	18x15	1	1	UPM1H4711MHH6
Panasonic FC (Surface Mfg)	10V	1000 $\mu$ F	0.043 $\Omega$	1205mA	12x16.5	1	1	EEVFC1A102LQ
	35V	330 $\mu$ F	0.065 $\Omega$	1205mA	12.5x16	1	1	EEVFC1V331LQ
	16V	330 $\mu$ F	0.150 $\Omega$	670mA	10x10.2	N/R	1	EEVFC1C331P
Oscon- SS SV	10V	330 $\mu$ F	0.025 $\Omega$	>3500mA	10.0x10.5	1	1	10SS330M
	10V	330 $\mu$ F	0.025 $\Omega$	>3800mA	10.3x10.3	1	1	10SV300M
	20V	150 $\mu$ F	0.024+2 $\Omega$	3600mA	10.3x10.3	2	2	20SV150M SV= Surface Mount
AVX Tantalum TPS	10V	330 $\mu$ F	0.100+2 $\Omega$	>2500mA	7.3Lx	2	1	TPSV337M010R0100
	10V	330 $\mu$ F	0.100+2 $\Omega$	>3000mA	4.3Wx	2	1	TPSV337M010R0060
	10V	220 $\mu$ F	0.095 $\Omega$	>2000mA	4.1H	2	2	TPSV227M0105R0100
Kemet T510/ T495	10V	330 $\mu$ F	0.033 $\Omega$	1400mA	7.3Lx5.7W	2	1	T510X337M010AS
	10V	220 $\mu$ F	0.07 $\Omega$ +2 =0.035 $\Omega$	>2000mA	x 4.0H	2	2	T495X227M010AS
Sprague 594D	10V	330 $\mu$ F	0.045 $\Omega$	2350mA	7.3Lx	2	1	4D337X0010R2T
	10V	220 $\mu$ F	0.065 $\Omega$	>2000mA	6.0Wx 4.1H	2	2	594D227X0010D2T

N/R –Not recommended. The ripple current rating and ESR does not meet the requirements.

**PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
PT6521B	OBSOLETE	SIP MODULE	EEK	14		TBD	Call TI	Call TI	-40 to 85		
PT6521ET	OBSOLETE	SIP MODULE	EEL	14		TBD	Call TI	Call TI			
PT6521F	OBSOLETE	SIP MODULE	EEF	14		TBD	Call TI	Call TI	-40 to 85		
PT6521M	OBSOLETE	SIP MODULE	EEM	14		TBD	Call TI	Call TI	-40 to 85		
PT6521Q	OBSOLETE	SIP MODULE	EEQ	14		TBD	Call TI	Call TI	-40 to 85		
PT6522E	OBSOLETE	SIP MODULE	EEC	14		TBD	Call TI	Call TI	-40 to 85		
PT6522ET	NRND	SIP MODULE	EEL	14		TBD	Call TI	Call TI			
PT6522F	NRND	SIP MODULE	EEF	14		TBD	Call TI	Call TI	-40 to 85		
PT6522M	NRND	SIP MODULE	EEM	14		TBD	Call TI	Call TI	-40 to 85		
PT6522Q	NRND	SIP MODULE	EEQ	14		TBD	Call TI	Call TI	-40 to 85		
PT6523B	LIFEBUY	SIP MODULE	EEK	14		TBD	Call TI	Call TI	-40 to 85		
PT6523ET	LIFEBUY	SIP MODULE	EEL	14		TBD	Call TI	Call TI			
PT6523F	LIFEBUY	SIP MODULE	EEF	14		TBD	Call TI	Call TI	-40 to 85		
PT6523G	LIFEBUY	SIP MODULE	EEG	14		TBD	Call TI	Call TI	-40 to 85		
PT6523L	LIFEBUY	SIP MODULE	EEL	14		TBD	Call TI	Call TI	-40 to 85		
PT6523M	LIFEBUY	SIP MODULE	EEM	14		TBD	Call TI	Call TI	-40 to 85		
PT6523Q	LIFEBUY	SIP MODULE	EEQ	14		TBD	Call TI	Call TI	-40 to 85		
PT6523R	LIFEBUY	SIP MODULE	EEE	14		TBD	Call TI	Call TI	-40 to 85		
PT6525B	NRND	SIP MODULE	EEK	14		TBD	Call TI	Call TI			
PT6525E	NRND	SIP MODULE	EEC	14		TBD	Call TI	Call TI			
PT6525F	NRND	SIP MODULE	EEF	14		TBD	Call TI	Call TI			
PT6525G	NRND	SIP MODULE	EEG	14		TBD	Call TI	Call TI			
PT6525L	NRND	SIP MODULE	EEL	14		TBD	Call TI	Call TI			
PT6525M	NRND	SIP MODULE	EEM	14		TBD	Call TI	Call TI			
PT6525P	NRND	SIP MODULE	EED	14		TBD	Call TI	Call TI			
PT6525Q	NRND	SIP MODULE	EEQ	14		TBD	Call TI	Call TI			
PT6525R	NRND	SIP MODULE	EEE	14		TBD	Call TI	Call TI			
PT6526B	LIFEBUY	SIP MODULE	EEK	14		TBD	Call TI	Call TI	-40 to 85		
PT6526ET	LIFEBUY	SIP MODULE	EEL	14		TBD	Call TI	Call TI			
PT6526M	LIFEBUY	SIP MODULE	EEM	14		TBD	Call TI	Call TI	-40 to 85		



Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead/Ball Finish (6)	MSL Peak Temp (3)	Op Temp (°C)	Device Marking (4/5)	Samples
PT6526Q	LIFEBUY	SIP MODULE	EEQ	14		TBD	Call TI	Call TI	-40 to 85		
PT6527D	NRND	SIP MODULE	EEA	14		TBD	Call TI	Call TI	-40 to 85		
PT6527L	NRND	SIP MODULE	EEL	14		TBD	Call TI	Call TI	-40 to 85		
PT6527M	NRND	SIP MODULE	EEM	14		TBD	Call TI	Call TI	-40 to 85		
PT6527Q	NRND	SIP MODULE	EEQ	14		TBD	Call TI	Call TI	-40 to 85		
PT6527R	NRND	SIP MODULE	EEE	14		TBD	Call TI	Call TI	-40 to 85		

(1) The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

(3) MSL, Peak Temp. - The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

(4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.

(5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.

(6) Lead/Ball Finish - Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead/Ball Finish values may wrap to two lines if the finish value exceeds the maximum column width.

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